**V. Naveen Kumar**

**Email Id**: [naveenkumar.vavilathota@gmail.com](mailto:naveenkumar.vavilathota@gmail.com)

**Contact**: 9494626227

**Objective:**

Seeking employment with a company engaged with Software testing where my expert knowledge and leadership experience will be effectively used.

**Technical Summary :**

* **Trained** in **Selenium web driver tool & Manual Testing with certification.**
* Knowledge on all kinds of Frameworks **Data Driven**, **Keyword driven**,**Pom.**
* Functional Testing, Integrating Testing, and **Regression** testing.
* Good exposure towards testing techniques and methodologies like **V Model** and **Water fall**.
* Good understanding of **SDLC, STLC, Test Case Designing** and **Execution**.
* Performing the Sanity testing, system testing, Writing the test scenarios.
* Executing the test cases, Posting the bugs, & Tracking them to closure.
* Handle the user defined functions using library files.
* Handle the flat files using the data driven technique.
* Involved in developing Test Cases & defect logging using internet tools.

**Educational Qualifications:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **LEVEL OF EDUCATION** | **INSTITUTION** | **BOARD/UNIVERSITY** | **YEAR OF PASSING** | **AGGREGATE**  **(In %)** |
| B. Tech  (E.C.E) | SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR. | Jawaharlal Nehru Technological University, ANANTAPUR | 2015 | 57.30 % |
| Intermediate  (A.P) | BOARD OF INTERMEDIATE EDUCATION ,ANDHRAPRADESH | SRI CHAITNANYA JR COLLEGE,  TIRUPATI | 2011 | 86.30 % |
| S.S.E | BOARD OF SECONDARY EDUCATION,  ANDHRAPRADESH | KUMAR .EM.HIGH  SCHOOL, TIRUPATI | 2009 | 84.50 % |

**Technical Skills:**

* Software Testing(Selenium + Manual)
* Basic SQL, **Core Java**
* MS-OFFICE

**Personality Traits:**

* Organization skills
* Self-motivation
* Self-confidence
* Team Spirit

## Operating Systems: Windows XP, Vista, 7, 8, 10.

## Academic Project : (B.Tech)

**Project Title** : IMPLEMENTATION OF HIGH SPEED MODIFIED WALLACE TREE MUTLIPLIER

**Project Description :** The Wallace tree multiplier is considered as faster than a simple array multiplier and is an efficient implementation of a digital circuit which is multiplies two integers. A Wallace tree multiplier is a parallel multiplier which uses the carry save addition algorithm to reduce the latency. There are many researchers have been worked on the design of increasingly more efficient multipliers. They aim at achieve higher speed and lower power consumption even while occupying reduced silicon area. The Wallace tree basically multiplies two unsigned integers. The new architecture enhances the speed performance of the widely acknowledged WTM.

**Achievements:**

* Got merit scholarships at High School and Intermediate level.
* Got Best Player Award in Cricket Tournament in Sitams College, Chittoor.

**Personal Profile:**

**Name**  : NAVEEN KUMAR V

**Father’s Name** : V MUNI RAJA

**Gender** : MALE

**Address**  : JAYANTHI VILLAGE,TEKUMANDHA POST,BANGARUPALYAM MANDAL,CHITTOOR DIST.

**Any Courses/Certifications Completed:** Software testing (Manual, Selenium Webdriver, Jira)

**Willing to relocate? (Y/N):** Yes

**Date of Birth** : 01-JULY-1994

**Language Known:** English, Hindi, Telugu

**Declaration:**

I hereby certify that all the information provided above is true to the best of my Knowledge.

**Place**:

**Date:** / /  **NAVEEN KUMAR.V**